



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Toshiyuki Kouchi

Serial No.: 09/821,163

Group Art Unit: 2133

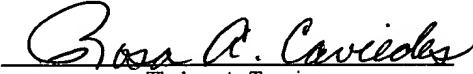
Filed: March 28, 2001

Examiner: Dooley, Matthew C.

Title: SEMICONDUCTOR INTEGRATED CIRCUIT

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April 22, 2004


Thelma A. Tannis

* * *

TRANSMITTAL OF FORMAL DRAWINGS

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ATTN: OFFICIAL DRAFTSPERSON
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith are six (6) sheets of formal drawings for the above-identified application. The attached sheet(s) of drawings include changes to Figs. 7 – 10 which now include "Prior Art". These sheets, which include Figs. 7 - 10, replace the original sheets including Figs. 7 - 10.

Please address all future communications regarding this application to:

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Please direct all telephone calls to Edward B. Weller (650) 833-2436.

Please charge any additional fees, including any fees necessary for extensions of time, or credit overpayment to Deposit Account No. 07-1896.

Respectfully submitted,
GRAY CARY WARE & FREIDENRICH LLP

Dated: April 22, 2004

By *Edward B. Weller*
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Appl. No. 09/821,163
Docket No. 2102475-991110
Response to Office Action of January 29, 2004

Amendments to the Drawings:

Please designate Figures 7-10 by – Prior Art --.

The attached sheet(s) of drawings include changes to Figs. 7 - 10. These sheets, which include Figs. 7 - 10, replaces the original sheets including Figs. 7 - 10.

Attachment: Replacement Sheet
Annotated Sheet Showing Changes

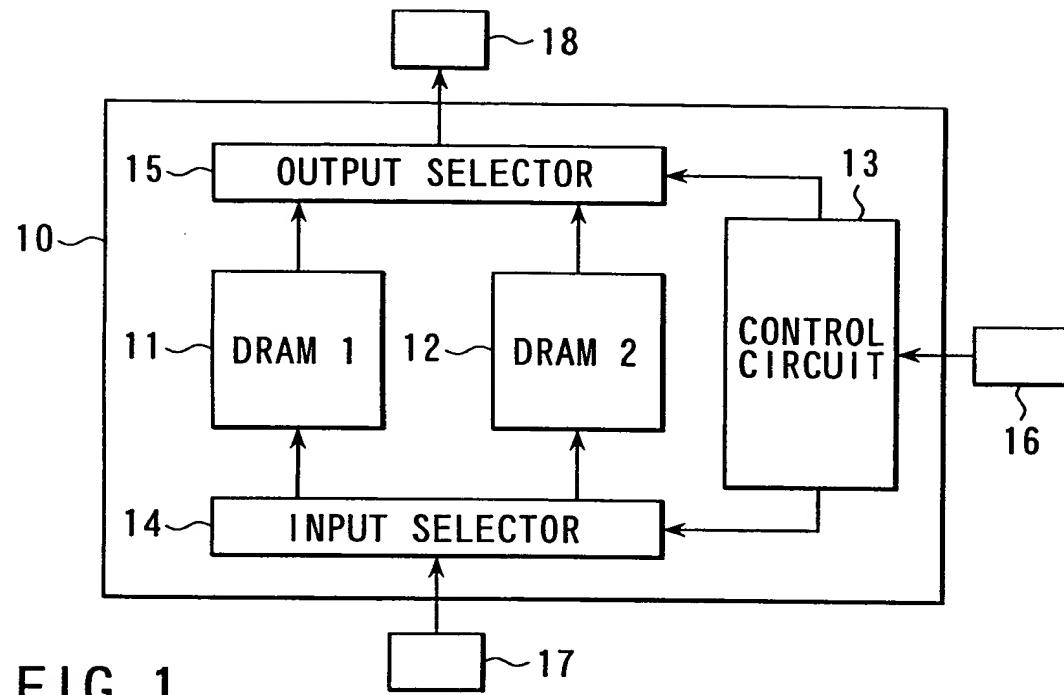


FIG. 1

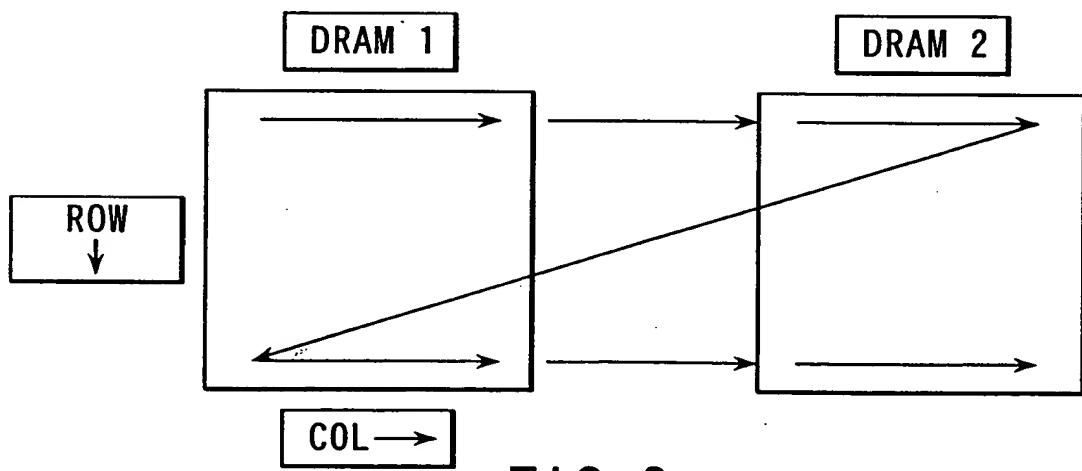


FIG. 2

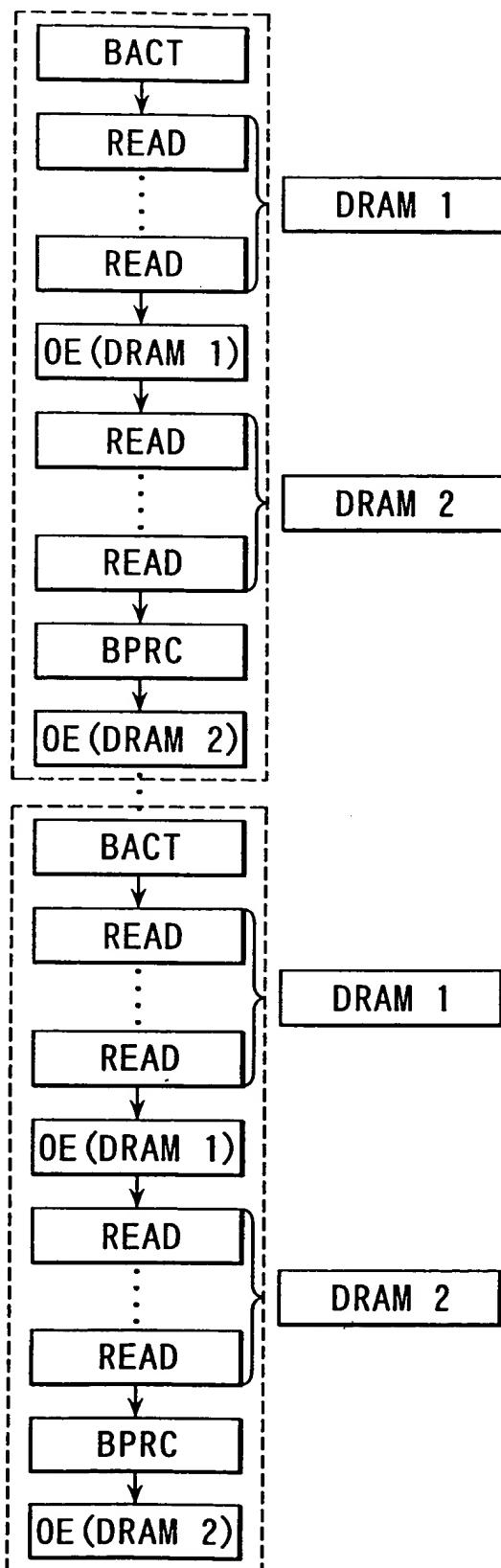


FIG. 3

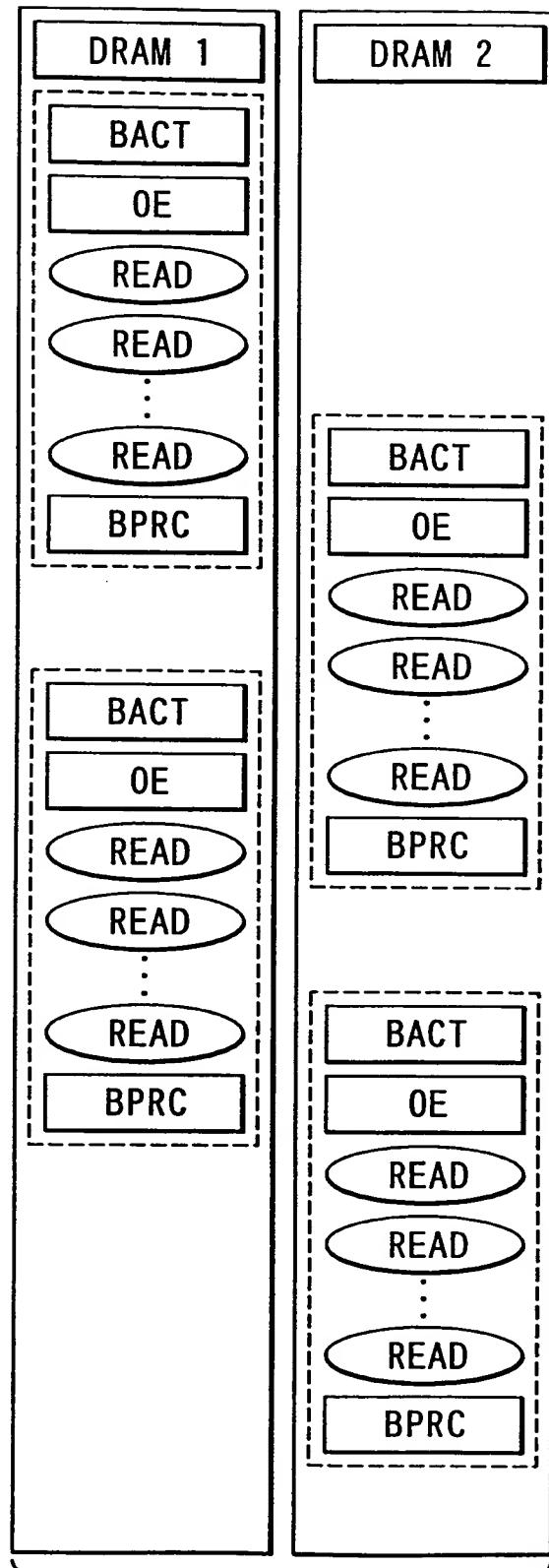


FIG. 6

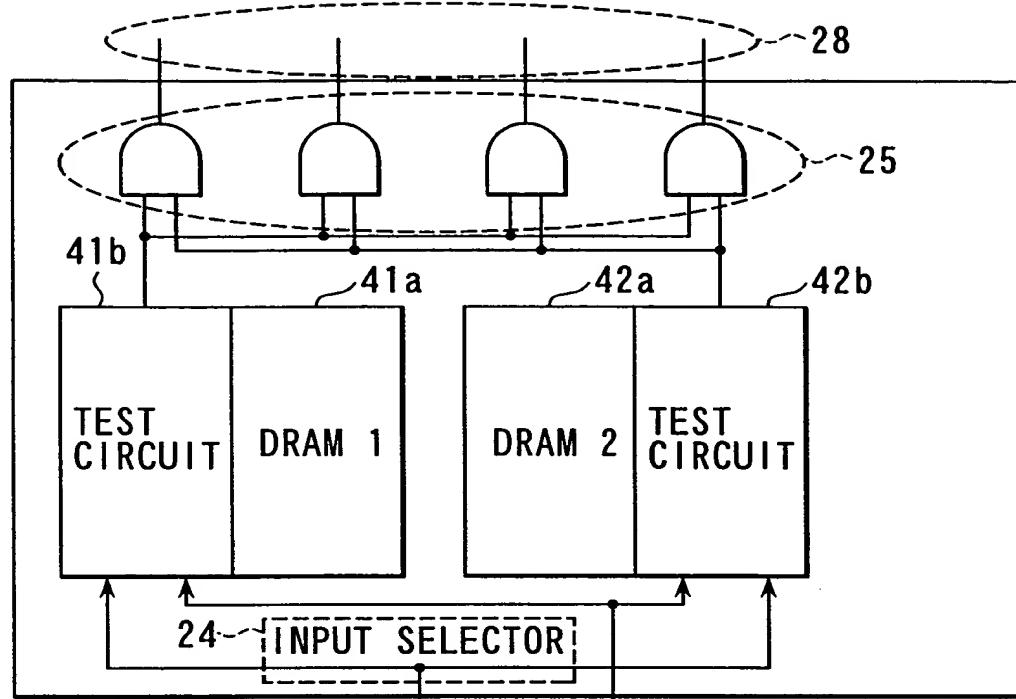


FIG. 4

27

26

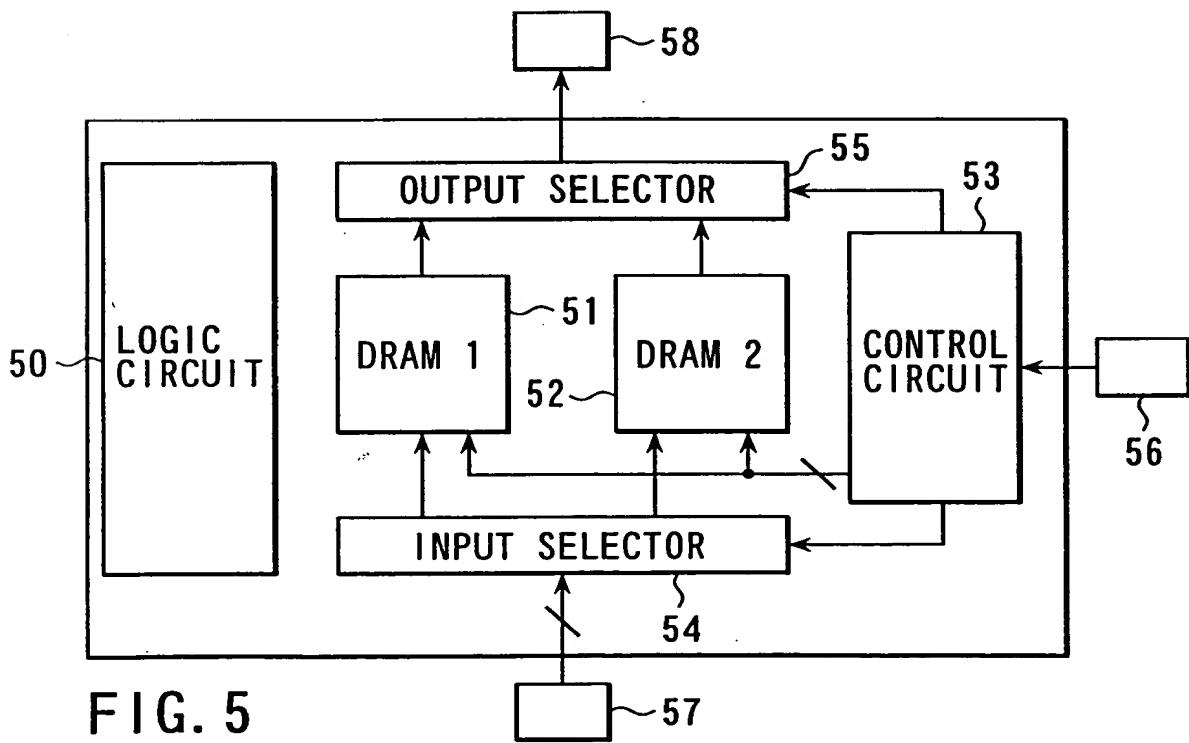
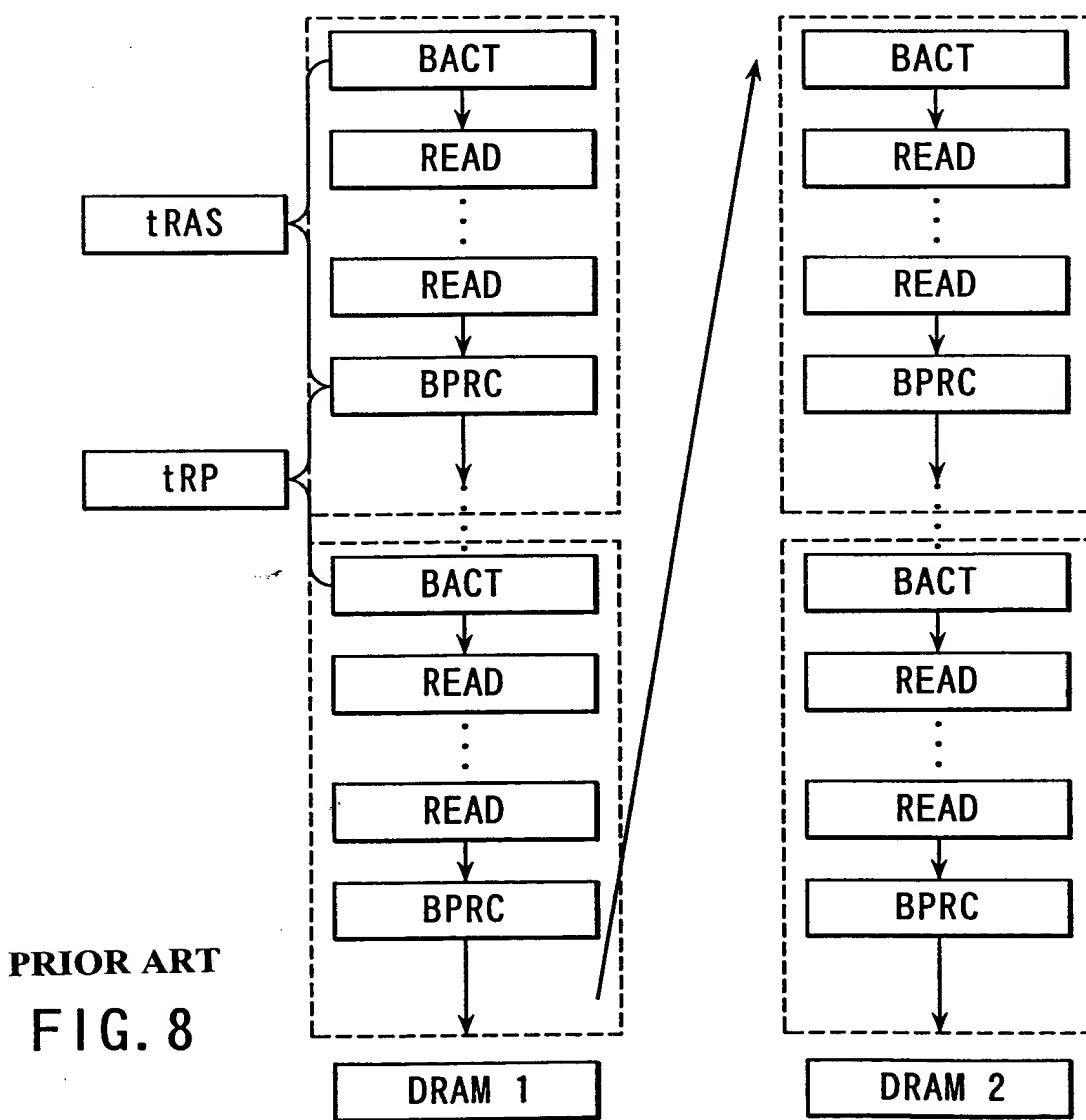
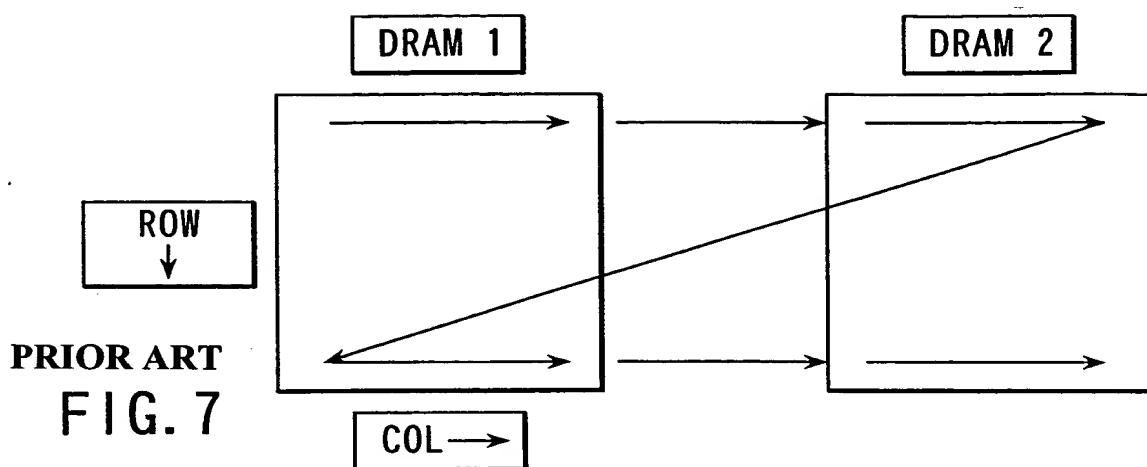


FIG. 5

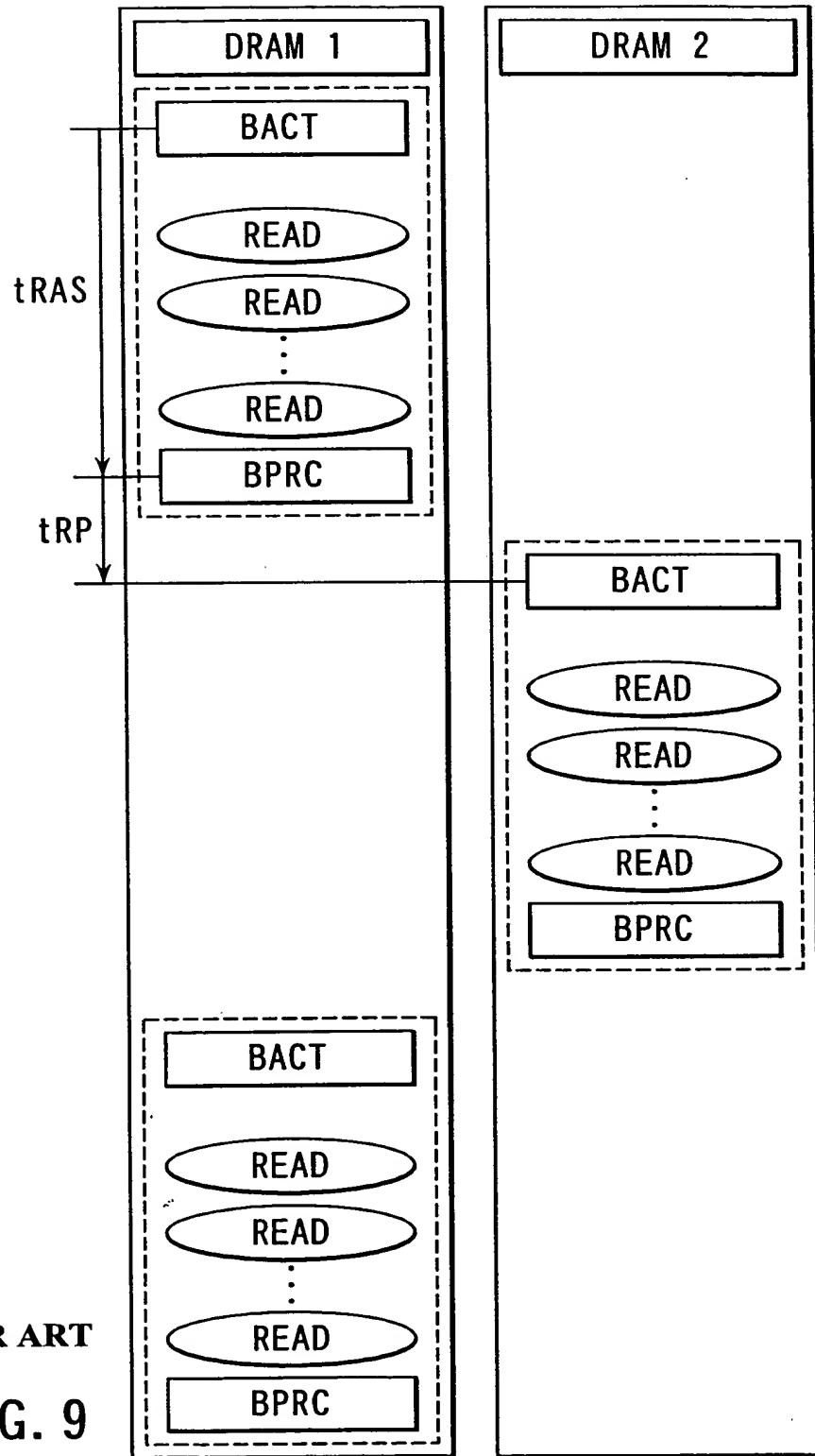
57

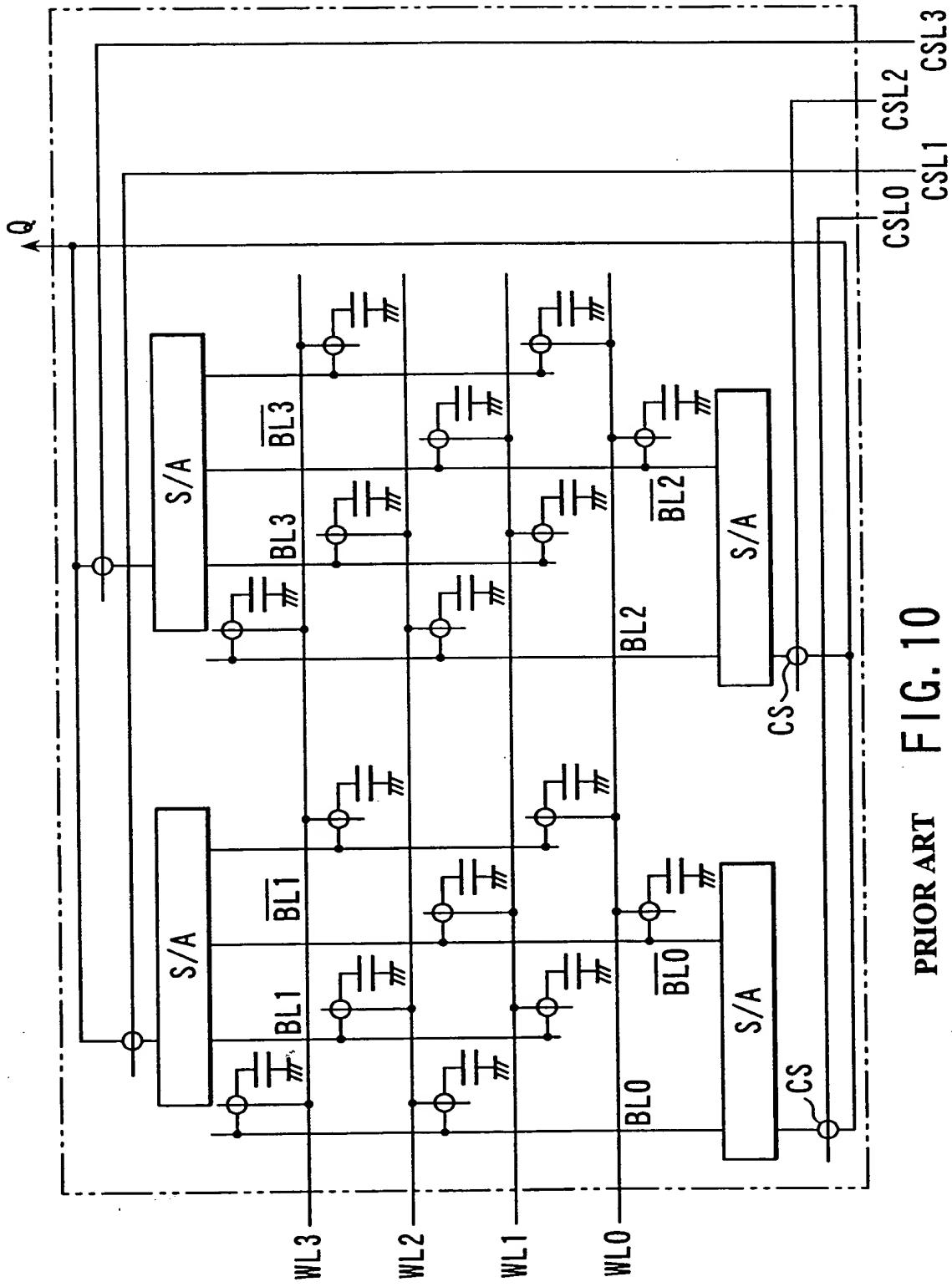




PRIOR ART

FIG. 9





PRIOR ART FIG. 10